RECIPIENT

SPECIFICATIONS

MODEL: SG-210STF

SPEC. No. : A13-115-0B

DATE: May.21.2013

SEIKO EPSON CORPORATION

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SPECIFICATIONS

1. Application

- (1) This document is applicable to the crystal oscillator that are delivered to from Seiko Epson Corp.
- (2) This product is compliant with RoHS Directive.
- (3) This Product supplied (and any technical information furnished, if any) by Seiko Epson Corporation shall not be used for the development and manufacture of weapon of mass destruction or for other military purposes. Making available such products and technology to any third party who may use such products or technologies for the said purposes are also prohibited.
- (4) This product listed here is designed as components or parts for electronics equipment in general consumer use. We do not expect that any of these products would be incorporated or otherwise used as a component or part for the equipment, which requires an extra high reliability, such as satellite, rocket and other space systems, and medical equipment, the functional purpose of which is to keep life.

2. Model

The model is SG-210STF.

3. Packing

It is subject to the packing standard of Seiko Epson Corp.

4. Warranty

Defective parts which are originated by us are replaced free of charge in case defects are found within 12 months after delivery.

5. Amendment and abolishment

Amendment and/or abolishment of this specification are subject to the agreement between both parties.

6. Contents

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7. MSL

MSL level 1

[1] Absolute maximum ratings

Parameter	Symbol	Value	Unit	Note
Supply voltage	Vcc-GND	-0.3 to +4.0	V	
Storage temperature *	T_stg	-40 to +125	°C	Stored as bare product after unpacking.
Input voltage	Vin	-0.3 to Vcc+0.3	V	ST Terminal

* Concerning the frequency change, please refer [8] Environmental and mechanical characteristics.

[2] Operating range

		Value				
Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Supply voltage	Vcc	1.6	-	3.6	V	
Supply voltage	GND	0.0	0.0	0.0	V	
Input voltage	Vin	GND	-	Vcc	V	
Operating temperature	T_use	-40	+25	+85	°C	
Output load condition	L_CMOS	-	-	15	pF	

• Start up time(0 % Vcc \rightarrow 90 % Vcc) of power source should be more than 150 µs.

• By-pass capacitor (0.01 μ F to 0.1 μ F) is connected near Vcc between Vcc and GND. (Refer to [11] Recommendable patterning)

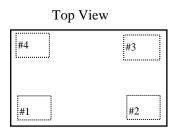
[3] Frequency characteristics

Output frequency (fo) range 1.0000 MHz to 60.0000 MHz

Parameter	Symbol	Value $[1 \times 10^{-6}]$	Note
Frequency tolerance *	<u>f</u> tol (OSC)	L: ± 50	T_use=-40 °C to +85 °C
Aging	F_aging	+ 3	T_use=+25 °C, Vcc=3.3 V First year

* This includes initial frequency tolerance, temperature characteristics, input voltage characteristics, and load characteristics, but excludes aging.

[4] Terminal assignment



Terminal name	Terminal No.	Terminal type.
ST	1	INPUT
GND	2	—
OUT	3	OUTPUT
Vcc	4	—

 \overline{ST} pin : High or open. \rightarrow Specified frequency output = enable.

 \overline{ST} pin : Low. \rightarrow Output is high impedance = disabled.

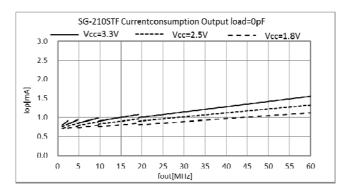
* When the \overline{ST} terminal is not controlled, it should be connected to the Vcc terminal.

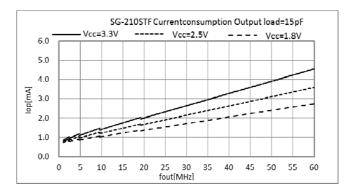
[5] Electrical characteristics

(Please see page 2 [2] Operating range)

		Va	lue		(
Parameter	Symbol	Min.	Max.	Unit	Note		
Start up time	tOSC	-	3	ms	t=0 at 90 % VCC	T	
		-	1.5		No load , 1 MHz \leq fo \leq 20 MHz	-	
		-	1.8		No load, 20 MHz $<$ fo \le 40 MHz	Vcc = 1.6 V to $2.2 V$	
		-	2.1		No load , 40 MHz $<$ fo \le 60 MHz		
		-	1.6		No load, 1 MHz \leq fo \leq 20 MHz		
Current consumption	ICC	-	2.0	mA	No load , 20 MHz $<$ fo \le 40 MHz	Vcc = 2.2 V to 2.7 V	
		-	2.4		No load , 40 MHz $<$ fo \le 60 MHz		
		-	1.8		No load , 1 MHz \leq fo \leq 20 MHz		
		-	2.2		No load , 20 MHz $<$ fo \le 40 MHz	Vcc = 2.7 V to 3.6 V	
		-	2.6		No load, $40 \text{ MHz} < \text{fo} \le 60 \text{ MHz}$		
		-	2.1			Vcc = 1.6 V to 2.2 V	
Standby current	I_std	-	2.5	μΑ	ST =GND Ta<+105 °C	Vcc = 2.2 V to 2.7 V	
		-	2.7			Vcc = 2.7 V to $3.6 V$	
Outerst size time	ta	-	3.0		$20 \% \text{ VCC} \rightarrow 80 \% \text{VCC}$		
Output rise time	tr	-	3.5	ns	$20 \% \text{ Vcc} \rightarrow 80 \% \text{ Vcc}$ Vcc=	1.8V±10%	
	tf	-	3.0		$80 \% \text{ Vcc} \rightarrow 20 \% \text{Vcc}$		
Output fall time	u	-	3.5	ns	80 % VCC \rightarrow 20 % VCC Vcc=	1.8V±10%	
Symmetry	SYM	45	55	%	50 % VCC Level		
High level output voltage	VOH	0.9 Vcc	-	V	IOH = -1 mA		
Low level output voltage	VOL	-	0.1 Vcc	V	IOL = 1 mA		
High level input voltage	VIH	0.8 Vcc	-	V	ST terminal		
Low level input voltage	VIL	-	0.2 Vcc	V	ST terminal		
Input current	IIH	-	10	μΑ	VIN = VCC		
	IIL	-10	-	μΑ	VIN = GND		
Output disable time	tstp	-	100	ns	\overline{ST} terminal High \rightarrow Low		
Output enable time	tsta	-	3	ms	\overline{ST} terminal Low \rightarrow High		

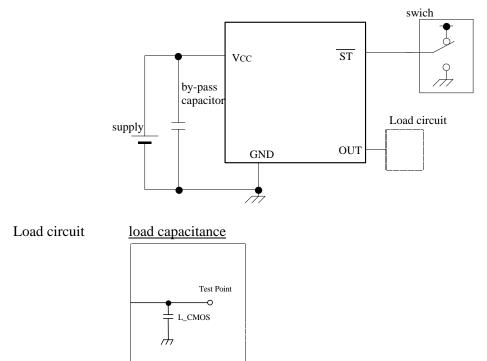
Refer to [6] Test circuit [7] Timing chart



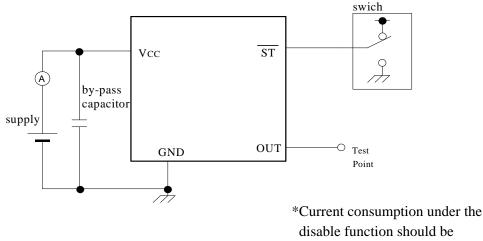


[6] Test circuit

1) Waveform observation



2) Current consumption



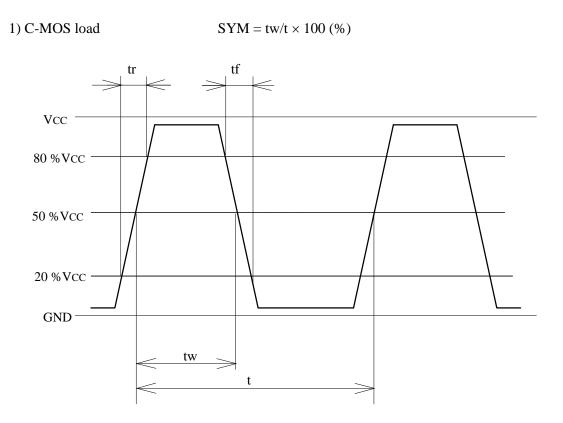
3) Condition

- (1) Oscilloscope
 - Band width should be minimum 5 times higher (wider) than measurement frequency.
 - Probe earth should be placed closely from test point and lead length should be as short as possible.

 $\overline{ST} = GND.$

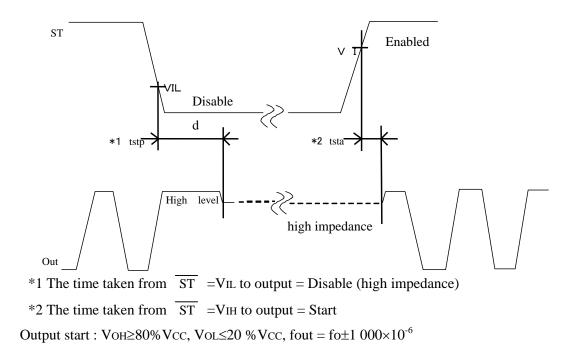
- * Recommendable to use miniature socket. (Don't use earth lead.)
- (2) L_CMOS also includes probe capacitance.
- (3) By-pass capacitor (0.01 μ F to 0.1 μ F) is placed closely between Vcc and GND.
- (4) Use the current meter whose internal impedance value is small.
- (5) Power supply
 - Start up time (0 %Vcc \rightarrow 90 %Vcc) of power source should be more than 150 µs.
 - Impedance of power supply should be as lowest as possible.

[7] Timing chart



2) ST function and timing

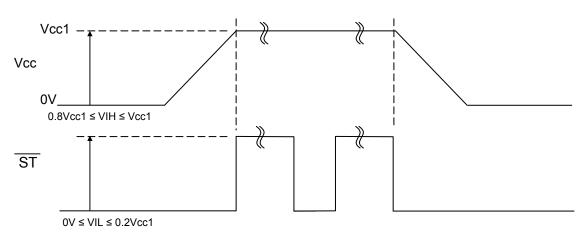
ST function Osc. circuit		Output status
High or Open	Oscillation	Specified frequency is output : Enable
Low	Oscillation stop	Output becomes high impedance : Disable



6

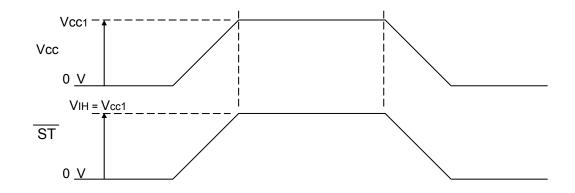
3) ST Control timing

ST function is used on the voltage below supply voltage.

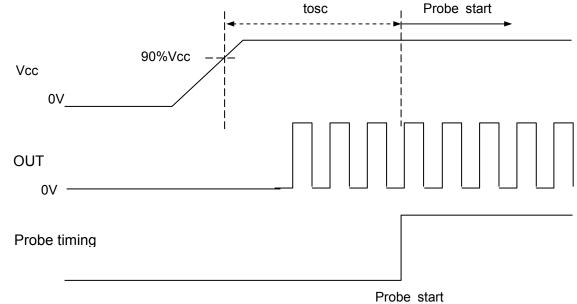


ST control timing differs from Vcc control timing

ST terminal is connected to Vcc terminal



4) Timing of an output frequency signal



[8] Environmental and mechanical characteristics

	N N		e *1	Test Conditions
No.	Item	$\Delta f / f *2$ [1 × 10 ⁻⁶]	Electrical characteristics	
1	High temperature storage	*3 ±20		+125 °C × 1 000 h
2	Low temperature storage	*3 ±10		-40 °C × 1 000 h
3	High temperature bias	*3 ±10		$+105 \text{ °C} \times V_{CC} \text{ Max.} \times 1\ 000 \text{ h}$
4	Low temperature bias	*3 ±10		-40 °C × V_{CC} Max. × 1 000 h
5	Temperature humidity bias	*3 ±10		+85 °C × 85 % RH × V_{CC} Max. × 1 000 h
6	Temperature cycle	*3 ±10		$-40 \text{ °C} \leftrightarrow +125 \text{ °C}$ 30 min. at each temperature 100 cycles
7	Resistance to soldering heat	±3		Convection reflow soldering furnace (3 time) Ref. IPC/JEDEC J-STD-020D.1
8	Shock	±3	Satisfy Item [5] after test.	150 g dummy Jig (EPSON TOYOCOM Standard) drop from 1 500 mm height on the Concrete 3 directions 10 times.
9	Vibration	±2		10 Hz to 55 Hz amplitude 0.75 mm 55 Hz to 500 Hz acceleration 98 m/s ² 10 Hz \rightarrow 500 Hz \rightarrow 10 Hz 15 min./cycle 6 h (2 hours , 3 directions)
10	Seal	1×10^{-9}	$Pa \cdot m^3 / s$	He leakage detector
11	Solderability	Termination must be 95 % covered with fresh solder		Dip termination into solder bath at +235 °C \pm 5 °C for 5 s. (Using Rosin Flux)
	Pull - off	No peeling-off at a solder part		10 N press for 10 s ± 1 s Ref. EIAJ ED-4702

(The company evaluation condition We evaluate it by the following examination item and examination condition.)

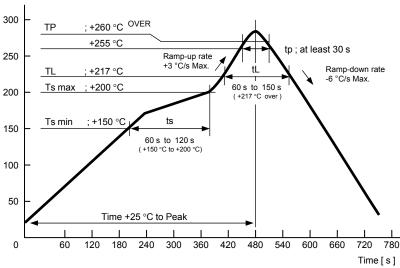
< Notes >

*1 Each test done independently.

*2 Measuring 2 h to 24 h later leaving in room temperature after each test.

*3 Initial value shall be measured after 24 h storage at room temperature after pre-conditioning. Pre-conditioning: Reflow (3 time)

Convection reflow condition (IPC/JEDEC J-STD-020D.1)



Temperature [°C]

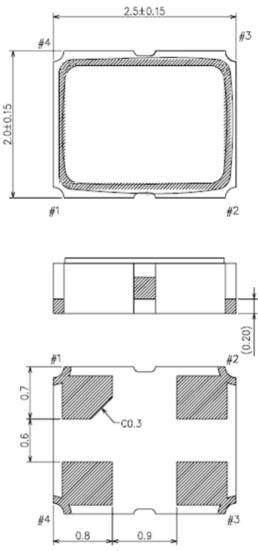
[9] Electro Static Discharge

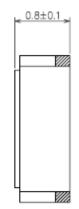
• ESD

Item	Electro Static Discharge	Test term
HBM	2 000 V Min.	EIAJ ED-4701-1 C111A,100pF,1.5KΩ, 3 time
MM	200 V Min.	EIAJ ED-4701-1 C111,200pF, 0Ω, 1 time

[10] Dimensions and marking layout

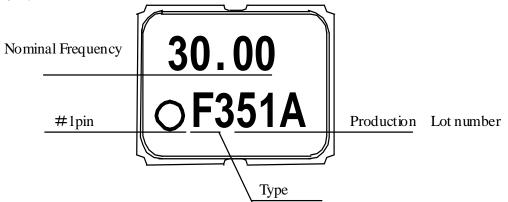
1) Dimensions





Terminal treatment : Au plating Unit : mm

2) Marking layout



- The above marking layout shows only marking contents and their approximate position and it is not for font, size and exact position.
- Output frequency shall indicate 5 digits (include decimal point), if the value of frequency over 5 digits, the least significant digits will be omitted.

[11] Notes

1) This device is made with C-MOS IC.

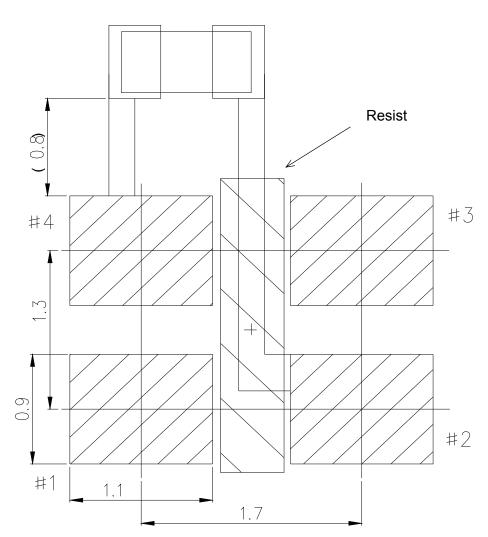
Please take necessary precautions to prevent damage due to electrical static discharge.

- 2) SEIKO EPSON recommends a 0.01 μ F to 0.1 μ F capacitor must be connected near Vcc between Vcc and GND to obtain stable operation and protect against power line ripple.
- 3) Vcc and GND pattern shall be as large as possible so that high frequency impedance shall be small.
- 4) SEIKO EPSON cannot recommend to put filtering element into power line so as to reduce noise. Oscillator might be unstable oscillation because high frequency impedance of power line become higher. When use filtering element, please verify electrical construction and or element's spec.
- 5) SEIKO EPSON doesn't recommend to power on from intermediate electric voltage or extreme fast power on, Those powering conditions may cause no oscillation or abnormal oscillation.
- 6) Power ripple: 200 mV P-P max. Start up time (0 %Vcc→90 %Vcc) of power source should be more than 150 μs.
- 7) A long output line may cause irregular output, so try to make the output line as short as possible.
- 8) Other high-level signal lines may cause incorrect operation, so please do not place high level signal line close to this device.
- 9) This device contains a crystal resonator, so please don't expose excessive shock or vibration. SEIKO EPSON recommends store device under normal temperature and humidity to keep the specification.
- An automatic insertion is available, however, the internal crystal resonator might be damaged in case that too much shock or vibration is applied by machine condition.
 Be sure to check your machine condition in advance.
- 11) Ultrasonic cleaning can be used on the SG-210SCF, however, since the oscillator might be damaged under some conditions, please exercise in advance.
- 12) SEIKO EPSON recommends to use and store under room temperature and normal humidity to secure frequency accuracy and prevent moisture.
- 13) ST -pin has pull-up resistor internally. The resistor value is switched depending on input voltage.
 Please refer to electrical characteristics.
- 14) Lid is electrically connected to GND. Please don't apply electrical voltage.

[12] Recommendable patterning

The soldering pad sample indicated as like following:

Soldering position (Unit : mm)



C 0.01uF~0.1uF

Output frequency list

SG-210STF

<u>G-21</u>	0511					
No.	Output frequency	Frequency stability [1×10 ⁻⁶]	Operating temperature range [°C]	Model No.	Date	Note
1	30 MHz	$L:\pm 50$	-40 ~+85	X1G004171003900	'13.05.21	
2	25 MHz	L:±50	-40 ~+85	X1G004171003300	'13.05.21	
3	50 MHz	L:±50	-40 ~+85	X1G004171004700	'13.05.21	

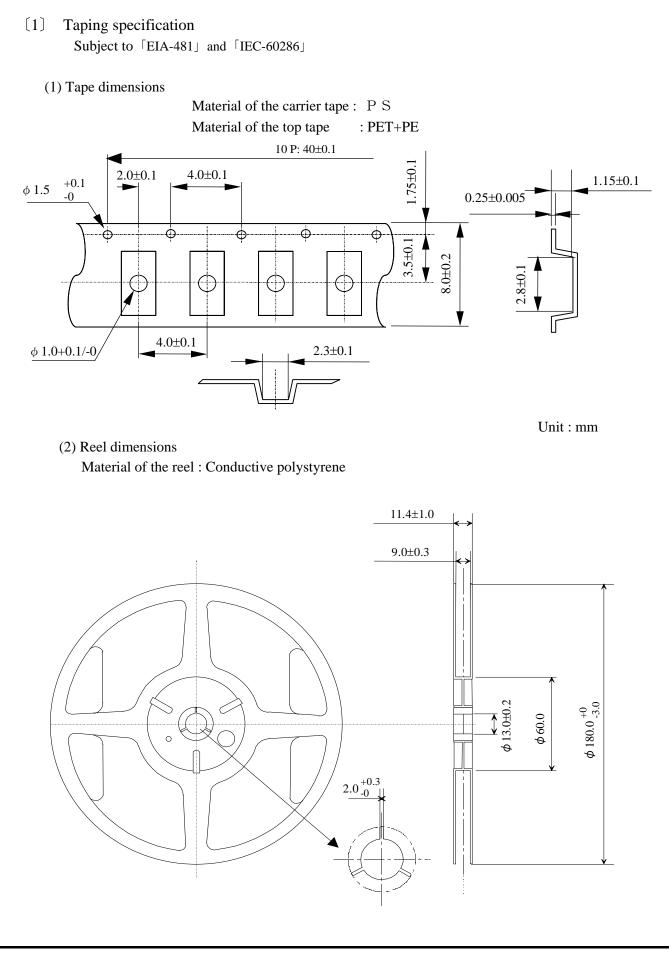
TAPING SPECIFICATION

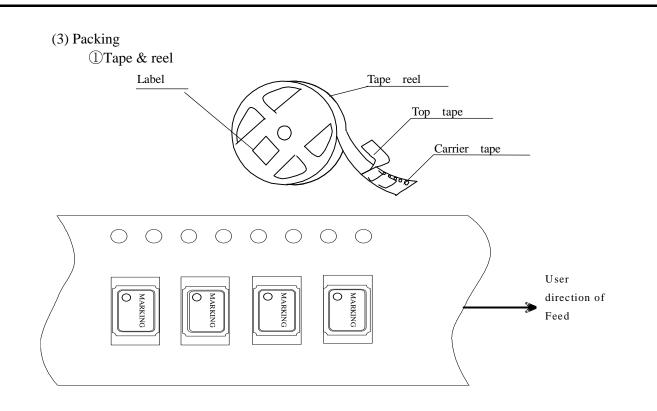
I. Application

This standard will apply to 2.5×2.0 Ceramic package. Spec : CG package

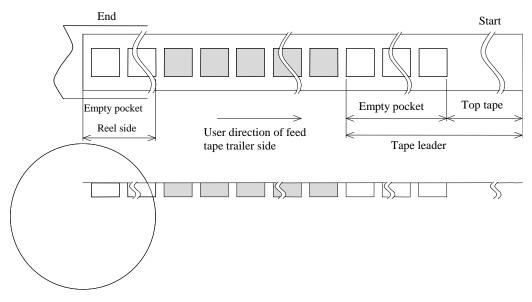
II. Contents

Item No.	Item	Page
[1]	Taping specification	1 to 2
[2]	Inner carton	3
[3]	Shipping carton	
[4]	Marking	4
[5]	Quantity	
[6]	Storage environment	
[7]	Handling	





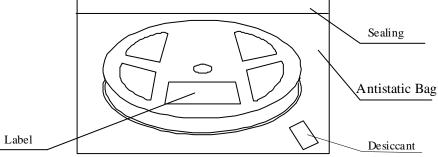
②Start & end point



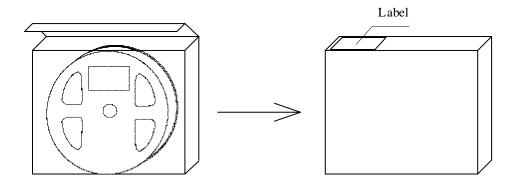
Item		Empty space		
Tape leader	Top tape	Min. 1 000 mm		
	Carrier tape	Min. 160 mm		
Tape trailer	Top tape	Min. 0 mm		
	Carrier tape	Min. 160 mm		

- (4) Peel force of the cover tape
 - (1) angle : cover tape during peel off and the direction of unreeling shall be 165° to 180° .
 - 2 peel speed : 300 mm / min.

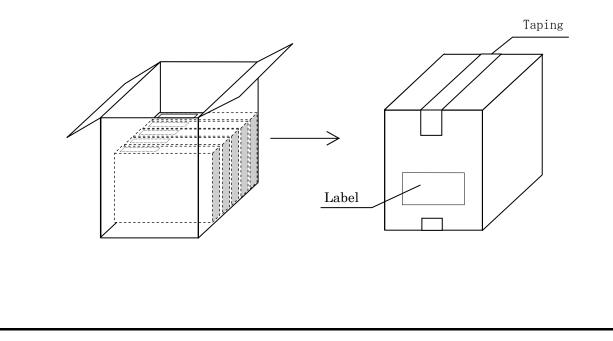
[2] Inner cartona) Packing to antistatic bag



b) Packing to inner carton



[3] Shipping carton



[4] Marking

- (1) Reel marking
 - Reel marking shall consist of :
 - 1) Parts name
 - 2) Quantity
 - 3) Manufacturing date or symbol
 - 4) Manufacturer's date or symbol
 - 5) Others (if necessary)
- (2) Inner carton marking
 - Same as reel marking.
- (3) Shipping carton marking
 - Shipping carton marking shall consist of :
 - 1) Parts name
 - 2) Quantity

[5] Quantity

• 3 000 pcs./reel

[6] Storage environment

- (1) Before open the packing, we recommend to keep less than +30 °C and 85 %RH of Humidity, and to use it less than 6 months after delivery.
- (2) We recommend to open Package in immediately before use. After open Package, We recommend to keeps less than 6 month. No need dry air before soldering work if it is less than temperature +30 °C, 85 humidity %RH.
- (3) Not to expose the sun.
- (4) Not to storage with some erosive chemicals.
- (5) Nothing is allowed to put on the reel or carton to prevent mechanical damage.

[7] Handling

• To handle with care to prevent the damage of tape, reel and products.

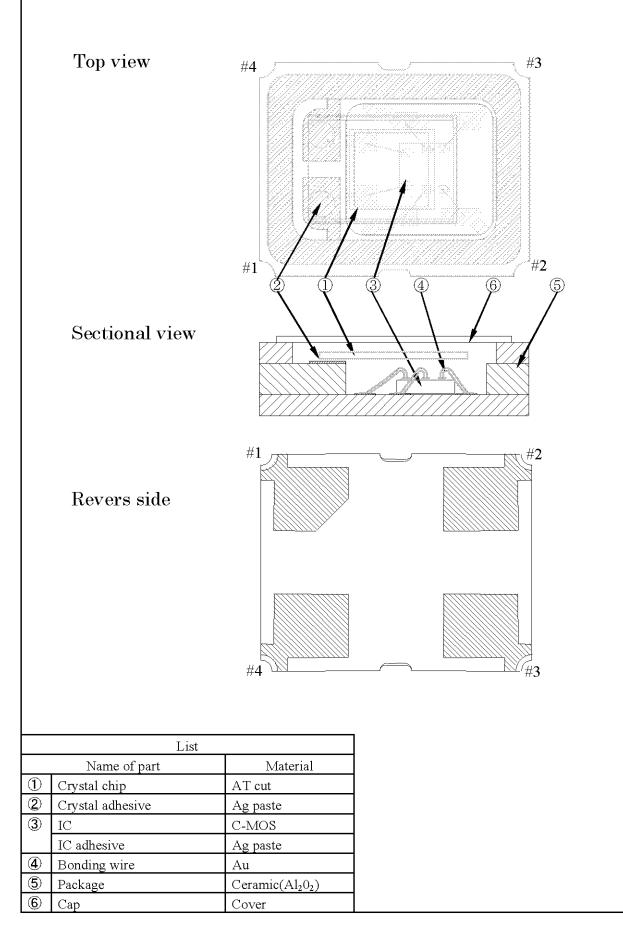
- PROCESS QUALITY CONTROL -

No. SG210STF - 01 - ASE - 1 CRYSTAL OSCILLATOR : SG-210STF

Dec.4.2012

Manufacturing process chart	No.	Section In Charge	Standards	Inspection, Control Item	Instruments	Inspection Methods	Record
Lid Crystal IC Base	1	Inspection Section	Purchasing Specification Incoming Inspection Standard	Appearance Dimension	Microscope	Sampling	Data sheet
1 1 In spection	2	China Plant (Production Section)	Manufacturing Instruction Sheet	Appearance Peeling Strength	Microscop <i>e</i> Scratch	Sampling	Data sheet
3 Sheet break	3	China Plant (Production Section)	Manufacturing Instruction Sheet	Appearance	Base array M/C	100% Inspection	Data sheet
2 Chip set 4 Parts Mounting (IC)	4	China Plant (Production Section)	Manufacturing Instruction Sheet	-	-	-	_
5 Wire Bonding	5	China Plant (Production Section)	Manufacturing Instruction Sheet	Bonding strength	Gauge	Sampling	Data sheet
6 Crystal-Mounting	6	China Plant (Production Section)	Manufacturing Instruction Sheet	Appearance	Microscope	Sampling	Data sheet
7 Annealing	7	China Plant (Production Section)	Manufacturing Instruction Sheet	-	-	-	-
8 Frequency Adjusting (Crystal)	8	China Plant (Production Section)	Manufacturing Instruction Sheet	Frequency	Counter	Sampling	Data sheet
9 Temporary Hermetic Sealing	9	China Plant (Production Section)	Manufacturing Instruction Sheet	_	-	-	-
(10) Hermetic Sealing	10	China Plant (Production Section)	Manufacturing Instruction Sheet	_	-	-	-
11 High Temp Treatment	11	China Plant (Production Section)	Manufacturing Instruction Sheet	_	_	-	-
12 Leakage	12	China Plant (Production Section)	Manufacturing Instruction Sheet	Leakage Inspection	Measuring equipment	100% Inspection	Data sheet
13 Marking	13	China Plant (Production Section)	Manufacturing Instruction Sheet	Appearance	Visual Inspection	Sampling	Data sheet
14 LDL Inspection	14	China Plant (Production Section)	Manufacturing Instruction Sheet	Characteristic Inspection	Measuring equipment	100% Inspection	Data sheet
15 Electrical Characteristic	15	China Plant (Production Section)	Manufacturing Instruction Sheet	Electrical Characteristic	Measuring equipment	100% Inspection	Data sheet
16 Visual Inspection	16	China Plant (Production Section)	Manufacturing Instruction Sheet	Appearance	Microscope	100% Inspection	Data sheet
17 Outgoing Inspection	17	China Plant (Inspection Section)	Delivery Specifications Outgoing Inspection Standard	Electrical Characteristic	Measuring equipment	Sampling	Data sheet
18 Taping	18	China Plant (Production Section)	Manufacturing Instruction Sheet	Tape peeling Strength Quantity Frequency check function	Peeling strength test machine	Sampling 100% Inspection	Data sheet
(19) Packing	19	Chaina Plant (Production Control Section)	Manufacturing Instruction Sheet Daily Shipping List	Customers Type Quantity	_	-	Delivery Slip

Structure diagram SG-210STF



RELIABILITY TEST DATA Product Name : SG-210STF

The Company evaluation condition

We evaluate environmental and mechanical characteristics by the following test condition .

No. ITEM		· · · · ·	VAL	TES		
		TEST CONDITIONS	Df/f [1 × 10 ⁻⁶]	Electrical characteristics	T Qty [n]	FAIL Qty [n]
1	ringii temperature	$+125 \ ^{\circ}C \times 1 \ 000 \ h$	*3 ±20		22	0
2	Low temperature	-40 °C × 1 000 h	*3 ±10		22	0
3	High temperature bias	+105 °C ×Vcc max × 1 000 h	*3 ±20		22	0
4	Low temperature bias	-40 °C × Vcc max × 1 000 h	*3 ±10		11	0
5	Temperature humidity bias	+85 °C × 85 % RH ×Vcc max × 1 000 h	*3 ±10	Satisfy	12	0
6	Temperature cycle	-40 °C⇔+125 °C 30 min at each temp. 100cycls	*3 ±10		22	0
7	Resistance to soldering heat	For comvention reflow soldering fumace(3 times) Ref.ICP/JEDEC J-STD-020D	±3	specification after test	22	0
8	Shock	150 g dummy (EPSON Standard) drop from 1 500 mm height on to the concrete 3 directions 10 times	±3		11	0
9	Vibration	10 Hz~55 Hz amplitude 0.75 mm 55 Hz~500 Hz acceleration 98 m/s2 10 Hz→500 Hz→10 Hz 15min/cycle 6 h (2h x 3 directions)	±2		11	0
10	Seal	He liakage detector	1×10^{-9} Pa·m ³ /s Max.		11	0
11	Solderability	Dip termination into solder bath at +235°C ±5°C for 5 sec.	Termination must be 95 % covered with fresh solder		3	0
12	Pull-off	10N press for 10s ± 1s Ref.EIAJ ED-4702	No Peeling-off at a solder part		3	0

Notes

1. *1 Each test done independently.

2. *2 Measuring 2 h to 24 h later leaving in room temperature after each test.

3. *3 Initial value shall be measured after 24 h storage at room temperature after pre-conditioning. Pre-conditioning: Reflow (3 time)



RELIABILITY TEST DATA Product Name : SG-210STF

