

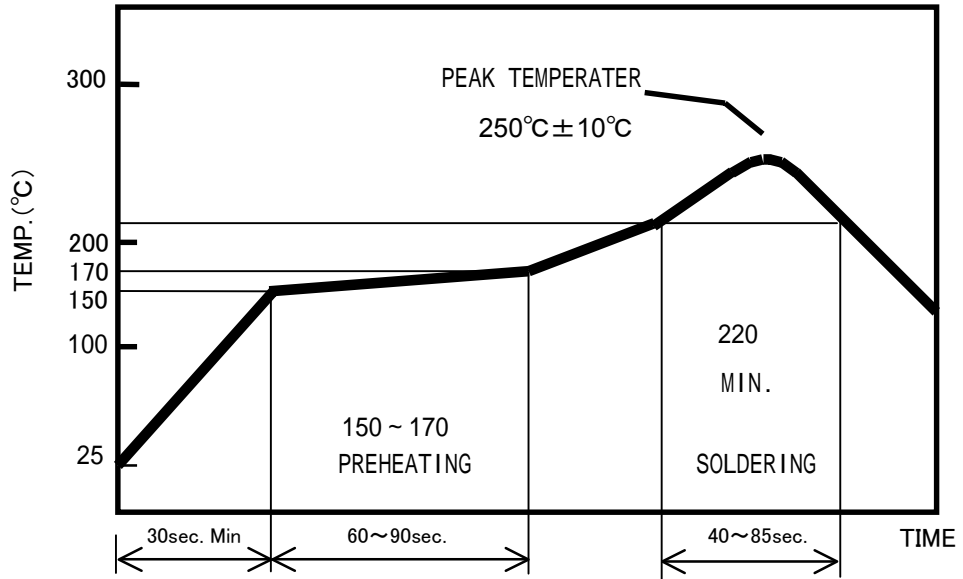
REV.NO.

登録 REGIST.	Supply Specification		名称 TITLE	仕番 SPEC. NO.				
版 年月日 DATE	作成部門 SECTION	作成年月日 DATE	CRYSTAL OSCILLATOR	TN4-21905				
1 05.06.07	36410	'05.06.07		頁 P.				
2	<div style="text-align: center;"><b>SPECIFICATIONS</b></div> <p>1. MODEL NAME TG-5001LA-62T</p> <p>2. ELECTRICAL SPECIFICATIONS</p> <p>2.1. Output frequency 26MHz</p> <p>2.2. Supply voltage DC +2.7V±0.1V</p> <p>2.3. Current drain 1.5 mA max.</p> <p>2.4. Output level 0.8 V<sub>p-p</sub> to 1.2 V<sub>p-p</sub> Clipped sinewave (DC-coupled)</p> <p>2.5. Load 10kΩ // 10pF ±10% each</p> <p>2.6. Operating temperature range -30°C to +85°C</p> <p>2.7. Storage temperature range -40°C to +85°C</p> <p>2.8. Frequency stability</p> <p>2.8.1. vs. Temperature ±2.5ppm max. / -30°C to +85°C (Referenced to +25°C)</p> <p>2.8.2. vs. Supply voltage ±0.2ppm max. / DC +2.7V±0.1V</p> <p>2.8.3. vs. Load ±0.2ppm max. / 10kΩ // 10pF ±10% each</p> <p>2.8.4. vs. Aging ±1.0ppm max. /year ±5.0ppm max. /5years</p> <p>2.9. Frequency control</p> <p>2.9.1. Frequency control range ±9.0ppm to ±16.0ppm /Vcont=+1.2V±1.0V Positive slope</p> <p>2.9.2. Control sensitivity 16ppm/V max.</p> <p>2.9.3. Linearity ±20% max.</p> <p>2.9.4. Input resistance 500kΩ min.</p> <p>2.10. Frequency tolerance ±1.0ppm max./Vcont=+1.2V,+25°C±2°C before reflow soldering</p> <p>2.11. Reflow frequency drift ±1.0ppm max./Vcont=+1.2V,+25°C±2°C 1 hour after 2 reflow soldering</p> <p>2.12. Duty cycle 40% to 60%</p> <p>2.13. Harmonics -10dBc max. (3<sup>rd</sup>), -15dBc max. (Other)</p> <p>2.14. SSB phase noise -105dBc/Hz max. @ 100Hz offset -125dBc/Hz max. @ 1kHz offset</p> <p>2.15. Start-up time 5.0ms max. / to 90% of final amplitude</p>				1 / 7			
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承認 APP. H.TAKANASHI								
照査 CHK'D H.HIRANO								
照査 CHK'D								
作成 DRAW Y.MARUYAMA	改版記事 DESCRIPTION							

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3. REFLOW SOLDERING PROFILE



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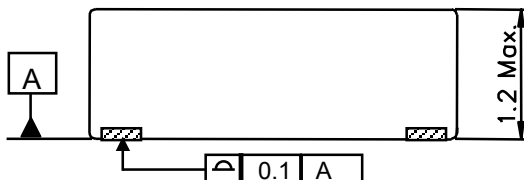
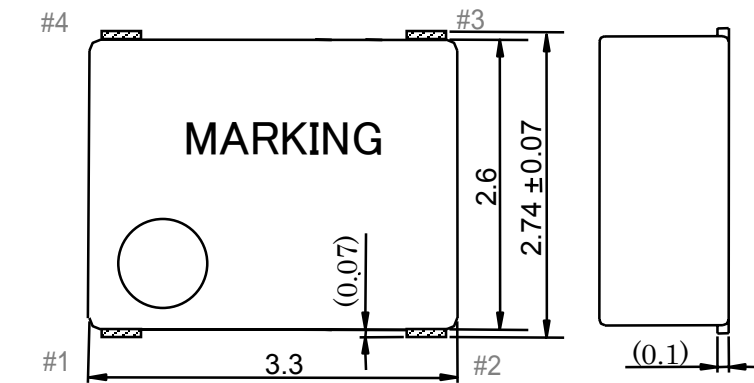
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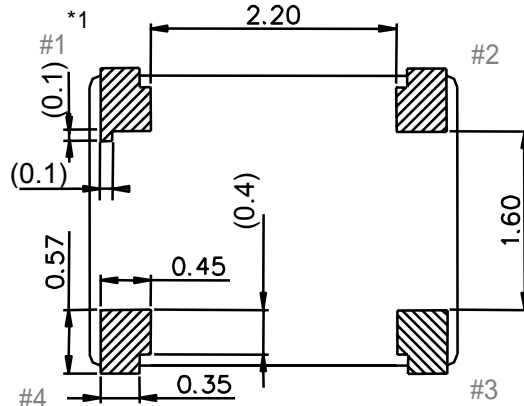
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4. OUTLINE DRAWING



Pin No.	Terminal
#1	Vcont
#2	GND
#3	OUT
#4	Vcc



Terminal treatment : Solder-Plating (Pb-Free)  
 Unit : mm  
 Tolerance : +/- 0.1mm  
 Dimensions in parenthesis is auxiliary value.

\*1.The terminal of #1\_pin may look being the same as #2 ~ #4\_pin

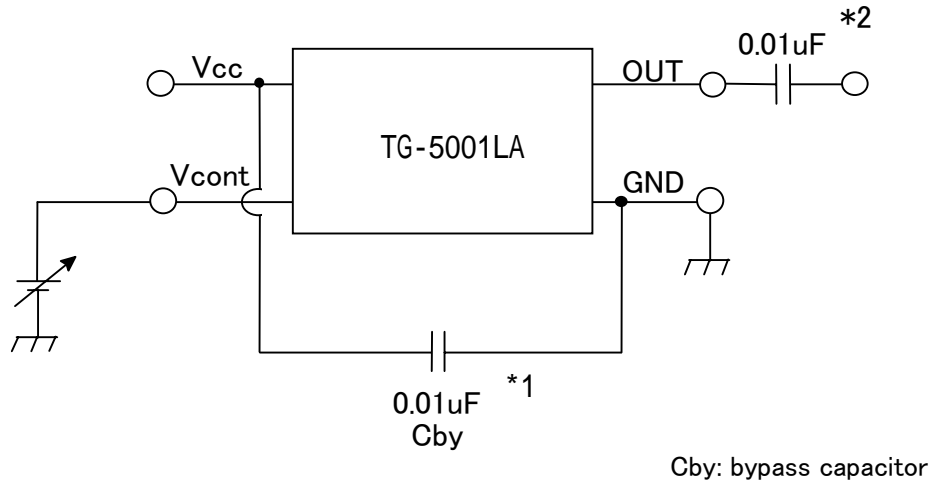
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5. CONNECTION



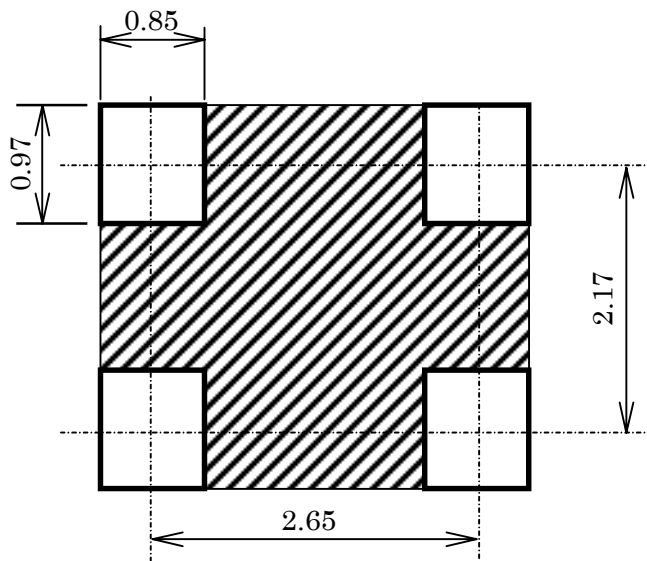
- \*1 Please connect capacitor(recommendation:0.01  $\mu$  F) between "Vcc" and "GND" terminal.
- \*2 Please connect capacitor(recommendation:0.01  $\mu$  F) between "OUT" terminal and load.
- \*3 This product has one chip LSI. Do not supply over +6V or negative voltage under  $-0.3V$  to "Vcc" terminal. Do not supply over  $V_{cc}+0.3V$  or negative voltage under  $-0.3V$  to "Vcont" terminal. Do not open "Vcont" terminal. Do not supply any voltages to "OUT" terminal.
- \*4 Do not supply any voltages in any way which differs from the above connection figure.

6. Recommendable patterning

For actual design work, please consider optimum condition together with mounting density, reliability of soldering and mount ability etc.  
Do not design any patterns except GND on the shaded area.

Soldering position

Unit : mm



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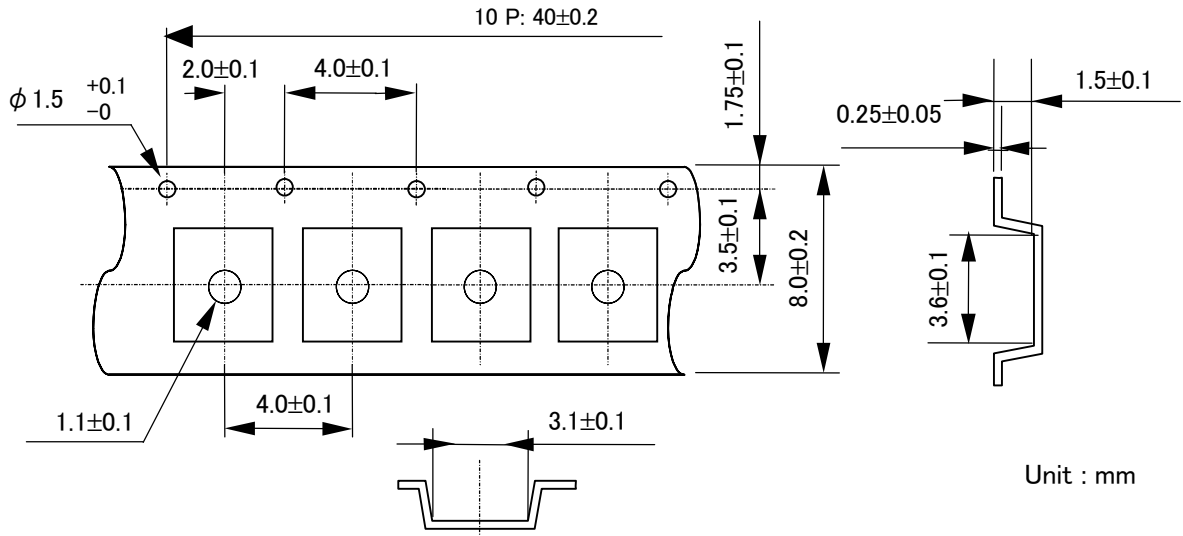
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7. TAPE & REEL PACKAGING SPECIFICATION

7.1. Embossed tape dimension & Outline drawings

Material of the Carrier Tape : PS

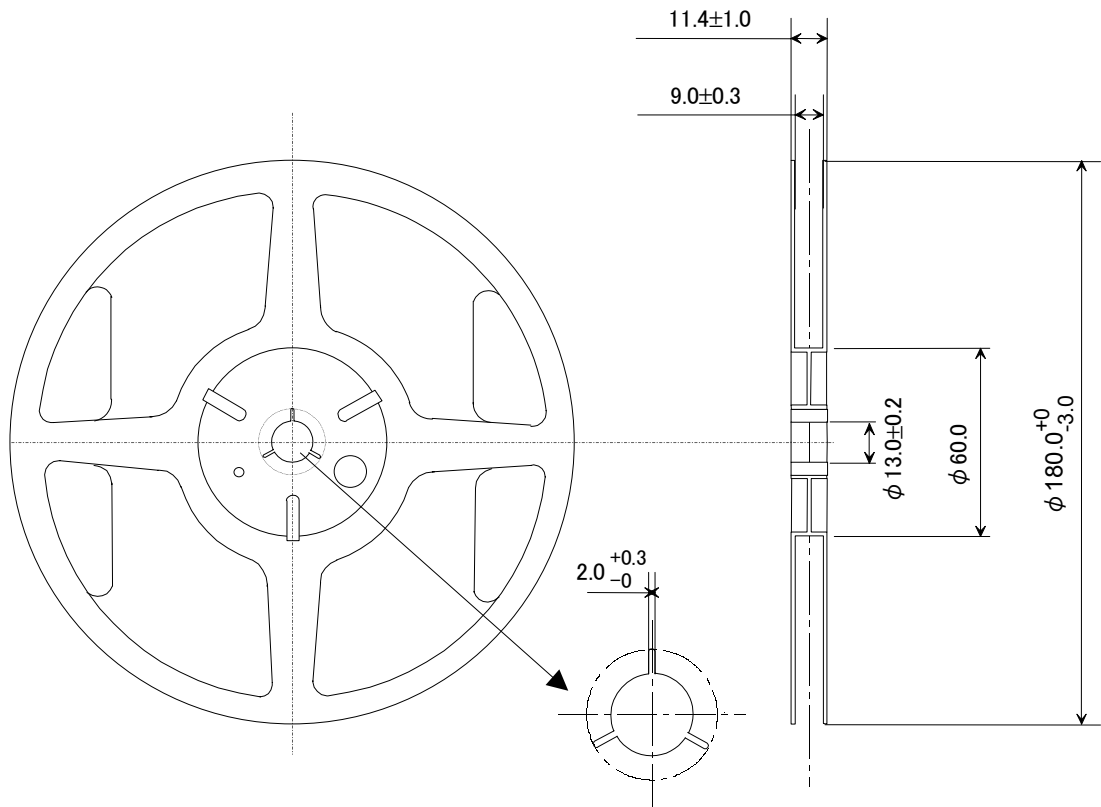
Material of the Top Tape : PET+PE



Unit : mm

7.2. Reel dimension & Outline drawing

Material of the Reel : PS



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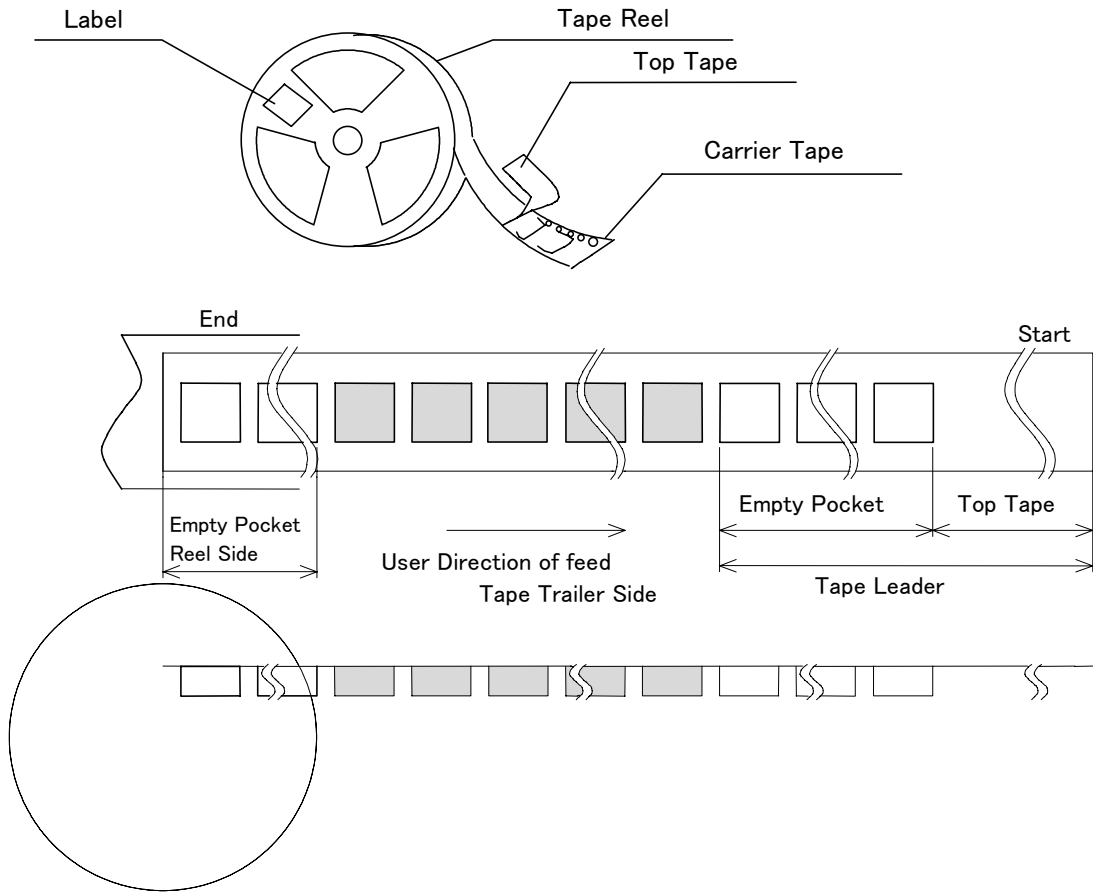
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## 7.3. Taping package



Item		Empty Space
Tape Leader	Top Tape	Min. 1 000 mm
	Carrier Tape	Min. 160 mm
Tape Trailer	Top Tape	Min. 0 mm
	Carrier Tape	Min. 160 mm

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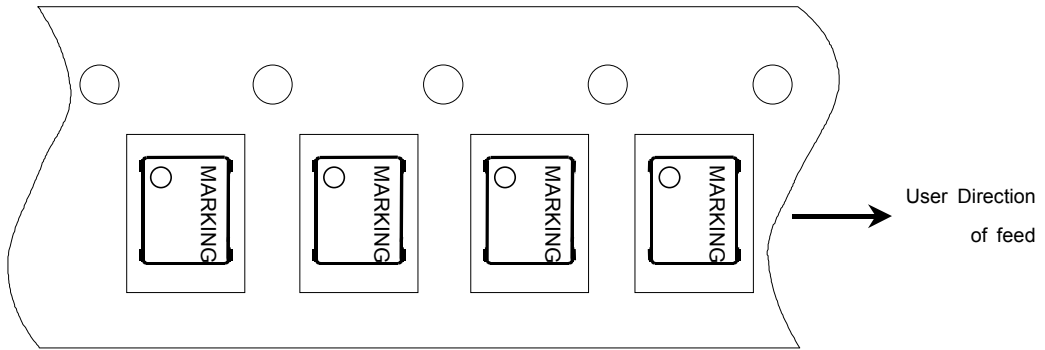
7.4. Quantity of components

2000pcs/reel (MAX.)  
500pcs/reel (MIN.)

NOTE

The above quantity is our standard packing size.  
In case of an odd sum as per the purchased quantity, the packing quantity might be below our standard minimum packing size.

7.5. Oscillator orientation : TOP VIEW



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