# Clock OSC

## SG-310SCF

## Product name SG-310SCF 8.000000 MHz L Product Number / Ordering code Q33310F

Q33310F700551xx

Please refer to the 8.Packing information about xx (last 2 digits)

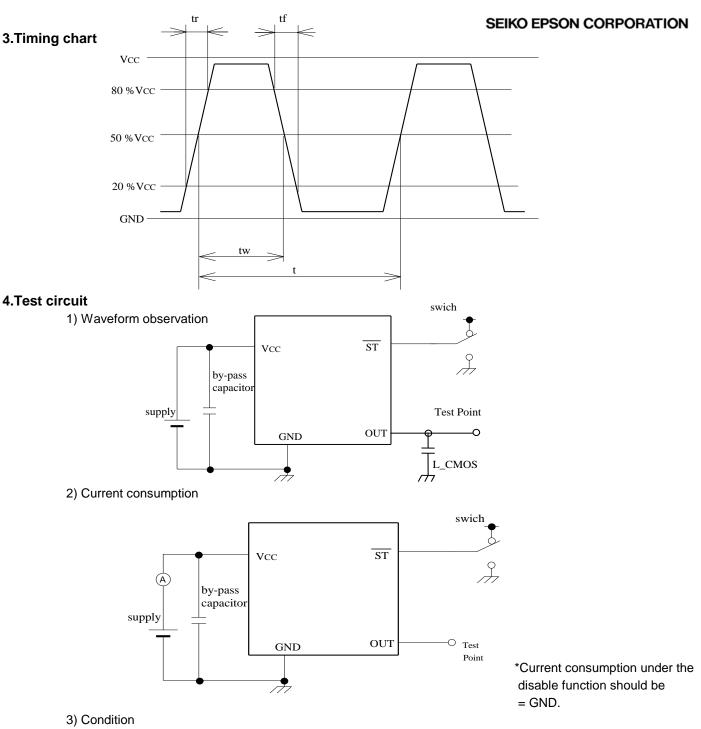
#### Output waveform CMOS

Pb free / Complies with EU RoHS directive

Reference weight Typ. 26	mg
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1.Absolute maximum ratings	i					
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Maximum supply voltage	Vcc-GND	-0.3	-	4.2	V	-
Storage temperature	T_stg	-40	-	125	٥C	Storage as single product
Input voltage	Vin	-0.3	-	Vcc+0.3	V	ST terminal

2.Specifications(characterist	tics)					
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Output frequency	fO		8.0000		MHz	
Supply voltage	Vcc	2.7	3.3	3.6	V	-
Operating temperature	T_use	-40	-	85	°C	-
Frequency tolerance	f_tol	-50	-	50	x10 <sup>-6</sup>	T_use
Current consumption	lcc	-	-	2	mA	No load condition
Stand-by current	I_std	-	-	2.0	μA	ST = GND
Symmetry	SYM	45	-	55	%	50% Vcc Level L_CMOS=<15pF
Output voltage	V <sub>OH</sub>	0.9Vcc	-	-		IOH=-3mA
	V <sub>OL</sub>	-	-	0.1Vcc		IOL=3mA
Output load condition	L_CMOS	-	-	15	pF	CMOS Load
Input voltage	V <sub>IH</sub>	0.8Vcc	-	-		ST terminal
	V <sub>IL</sub>	-	-	0.2Vcc		ST terminal
Rise time	t <sub>r</sub>	-	-	4	ns	0.2Vcc to 0.8Vcc Level, L_CMOS=15pF
Fall time	tf	-	-	4	ns	0.2Vcc to 0.8Vcc Level, L_CMOS=15pF
Start-up time	t_str	-	-	10	ms	t = 0 at 0.9Vcc
Jitter	t <sub>DJ</sub>	-	TBD	-	ps	Deterministic Jitter
	T <sub>RJ</sub>	-	TBD	-	ps	Random Jitter
	t <sub>RMS</sub>	-	TBD	-	ps	δ(RMS of total distribution)
	t <sub>p-p</sub>	-	TBD	-	ps	Peak to Peak
	t <sub>acc</sub>	-	TBD	-	ps	Accumulated Jitter(δ) n=2 to 50000 cycles
Phase jitter	t <sub>PJ</sub>	-	TBD	-	ps	Off set Frequency: 12kHz to 20MHz
Phase noise	L(f)	-	TBD	-	dBc/Hz	Off set 1Hz
		-	TBD	-	dBc/Hz	Off set 10Hz
		-	TBD	-	dBc/Hz	Off set 100Hz
		-	TBD	-	dBc/Hz	Off set 1kHz
		-	TBD	-	dBc/Hz	Off set 10kHz
		-	TBD	-	dBc/Hz	Off set 100kHz
		-	TBD	-	dBc/Hz	Off set 1MHz
Frequency aging	f_age	-5	-	5	x10 <sup>-6</sup>	@+25°C first year
		-	-	-		-



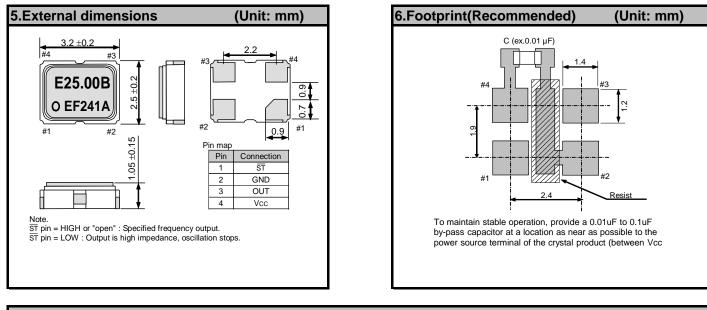
(1) Oscilloscope

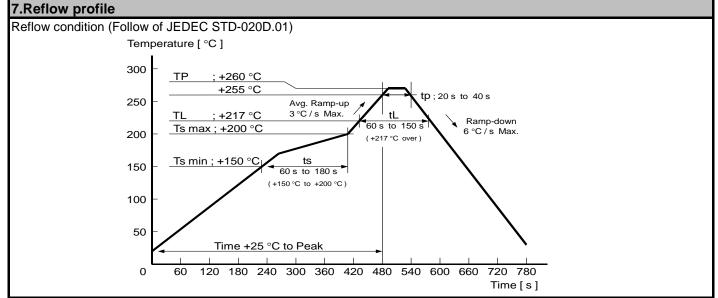
· Band width should be minimum 5 times higher (wider) than measurement frequency.

· Probe earth should be placed closely from test point and lead length should be as short as possible

- \* Recommendable to use miniature socket. (Don't use earth lead.)
- (2) L\_CMOS also includes probe capacitance.
- (3) By-pass capacitor (0.01 mF to 0.1 mF) is placed closely between VCC and GND.
- (4) Use the current meter whose internal impedance value is small.
- (5) Power supply
- Start up time (0 %VCC ® 90 %VCC) of power source should be more than 150 ms.
- $\cdot$  Impedance of power supply should be as lowest as possible.

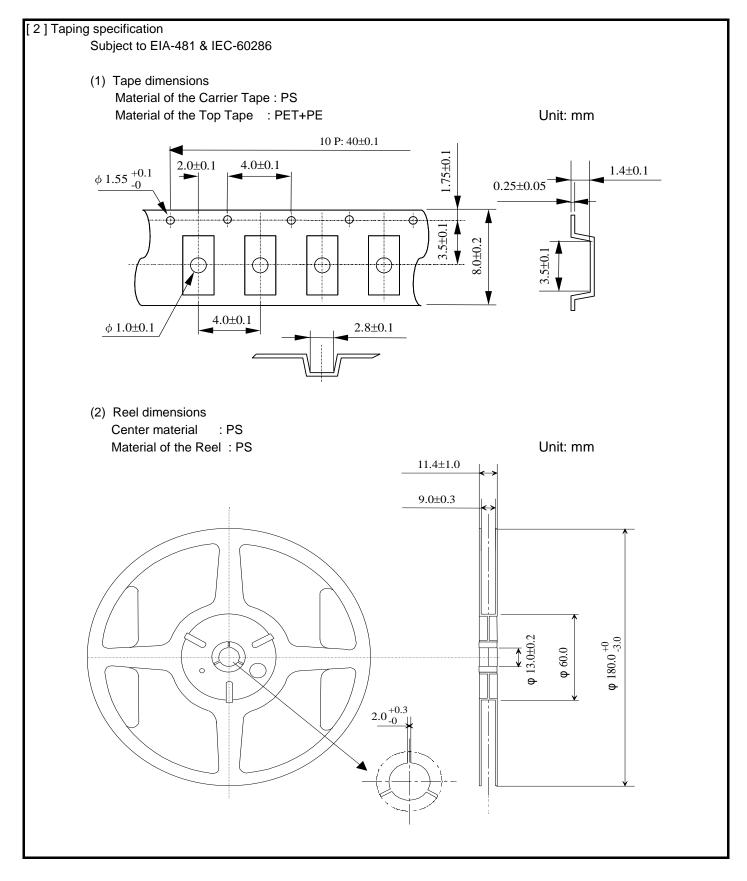
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## 8.Packing information

1 ]Product number last 2 digits code(xx) description			The recommended code is "00"		
	Q33310F	700551xx			
	Code	Condition	Code	Condition	
	01	Any Q'ty vinyl bag(Tape cut)	13	500pcs / Reel	
	11	Any Q'ty / Reel	14	1000pcs / Reel	
	12	250pcs / Reel	00	2000pcs / Reel	



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- / Traffic control equipment
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