

SEIKO EPSON CORPORATION

CRYSTAL OSCILLATOR (SPXO) OUTPUT : LV-PECL, LVDS

SG2016EGN/VGN SG2520EGN/VGN

 Frequency range 	:	25 MHz to 500 MHz	
 Supply voltage 	:	1.8 V Typ. (LVDS only) / 2.5 V Typ. / 3.3 V Typ.	
 Frequency tolerance 	:	±25 × 10 ⁻⁶ , ±50 × 10 ⁻⁶	0000/0501
 Operating temperature 	:	-40 °C to +85 °C, -40 °C to +105 °C	SG2016EGN SG2016VGN
 Function 	:	Output enable (OE) or Standby (\overline{ST})	$(2.0 \times 1.6 \times 0.63 \text{ mm})$
 Phase jitter 	:	50 fs Max. (391 MHz < fo \leq 500 MHz, V _{CC} = 2.5 V, 3.3 V)	(



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Product Number SG2016EGN: X1G006131xxxx15 SG2016VGN: X1G006111xxxx15 SG2520EGN: X1G005881xxxx15 SG2520VGN: X1G005901xxxx15



SG2520EGN SG2520VGN (2.5 × 2.0 × 0.74 mm)

Specifications (characteristics)

			Specifications		_	
Item	Symbol	LV-PECL	LVDS		Condition	s / Remarks
	Symbol	SG2016EGN SG2016VGN / SG2520VGN				
Output frequency range	fo	25 MHz to 500 MHz			Please contact us for availa	able frequencies.
Supply voltage	Vcc		C: 3.3 V ± 5 % D: 2.5 V ± 5 %	E: 1.8 V ± 5 %		
Storage temperature	T_stg		-55 °C to +125 °C			
Operating temperature	T_use	G:	-40 °C to +85 °C, H: -40 °C to	+105 °C		
Frequency tolerance	f_tol	D: ±25 × 10 ⁻⁶ Max. J: ±50 × 10 ⁻⁶ Max.			Includes initial frequency tolerance, frequency / temperature characteristics, frequency / voltage coefficient and 10 years aging (+25 °C)	
Current consumption	Icc	60 mA Max.	_	1	OE or $\overline{ST} = V_{CC}$, L_ECL = 5	Ω 00
		-	25 mA / 30 mA / 25 mA Max. 28 mA / 35 mA / 28 mA Max. 28 mA / 35 mA / 30 mA Max.	25 mA / – / 25 mA Max.	$25 \text{ MHz} \le \text{fo} < 212 \text{ MHz}$ $212 \text{ MHz} \le \text{fo} < 392 \text{ MHz}$ $392 \text{ MHz} \le \text{fo} \le 500 \text{ MHz}$	OE or ST = V _{CC} , Output option: A / B / C
Disable current	I_dis	35 mA Max.	20 mA Max. 20 mA M	ax	OE = GND	
		00 11/ 11/10/2	30 µA Max.	ал.	$\overline{ST} = GND$, T_use Max. = +85 °C	
Stand-by current	I_std		60 µA Max.		<u>ST</u> = GND, T_use Max. = +105 °C	
Symmetry	SYM		45 % to 55 %		At output crossing point	
Output voltage (LV-PECL)	V _{OH} V _{OL}	V _{CC} - 1.1 V Min. V _{CC} - 1.5 V Max.			Output option: A, DC characteristic	
		0.8 V to 2.0 V	500 mV to 900 mV	500 mV to 900 mV	Output option: A	
Differential swing	Vsw	-	800 mV to 1 600 mV	-	Output option: B	
		-	600 mV to 1 200 mV	600 mV to 1 200 mV	Output option: C	
			250 mV to 450 mV	250 mV to 450 mV	Output option: A	Differential output voltage,
	Vod	-	400 mV to 800 mV	-	Output option: B	Vod1, Vod2
Output voltage (LVDS)	dVop	_	300 mV to 600 mV 50 mV M	300 mV to 600 mV	Output option: C dVop = Vop1 - Vop2	-
	Vos		1.15 V to 1.35 V	0.65 V to 0.85 V	Offset voltage, V _{OS1} , V _{OS2}	
	dVos		50 mV Max.		$dV_{OS} = V_{OS1} - V_{OS2} $	
	L ECL	50 Ω –			Terminated to V_{CC} - 2.0 V	
Output load condition	L_LVDS	- 100 Ω			Connected between OUT and OUT	
lange of some life and	VIH	70 % V _{CC} Min. 30 % V _{CC} Max.			OE or ST terminal	
Input voltage	VIL					
Rise/Fall times	tr/tf		0.35 ns Max.		LV-PECL: 20 % - 80 % (V _{OH} - V _{OL}) LVDS: 20 % - 80 % differential output peak to peak	
Start-up time	t_str		10 ms Max.		t = 0 at 90 % V _{CC}	
		250 fs Max.	250 fs Max.	400 fs Max.		Offset frequency
		90 fs Max.	100 fs Max.	130 fs Max.	$100 \text{ MHz} \le \text{fo} \le 156 \text{ MHz}$	fo < 50 MHz:
Phase jitter	t _{PJ}	70 fs Max.	60 fs Max.	70 fs Max.	156 MHz < fo ≤ 212 MHz	12 kHz to 5 MH
	l I	60 fs Max. 50 fs Max.	50 fs Max.	60 fs Max.	$\frac{212 \text{ MHz} < \text{fo} \le 391 \text{ MHz}}{391 \text{ MHz} < \text{fo} \le 500 \text{ MHz}}$	fo ≥ 50 MHz: 12 kHz to 20 MH
					391 MHZ < 10 5 500 MHZ	
-	_		<u>1Hz C D H P Z A</u>			
(Standard form) ①		3	456789			
			ECL, V: LVDS) ③Frequency			
	-	<u> </u>	Function 8Output disable ty		Output option	
	y voltage	5Freq. tolerar		⑦Function	9Output option	
C 3.3 D 2.5		$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$			SG2016EGN / SG2520EGN SG2016VGN / SG2520VGN A Default V _{OD} = 250 mV to 450 mV	
E* 1.8					B* –	$V_{OD} = 250 \text{ mV} \text{ to } 450 \text{ mV}$ $V_{OD} = 400 \text{ mV} \text{ to } 800 \text{ mV}$
• "E' is offy of SG2016/ON and SG2520/GN C C − Vop = 300 mV to 600						
				_	*Not available for V _{CC} = 1.8 V Typ.	· ·
External dimension			(Unit:r	nm) Footprin	t (Recommended)	(Unit:mm
SG2520EGN / SG2520VG	N SG20	16EGN / SG2016V 2.0±0.15	GN			
2.5±0.15 #6 #5 #4	#6	#5 #4	Pin map		ΙΕΙΙΒΙ	
μ. 	15		Pin Connection			
		e P Ilbert				

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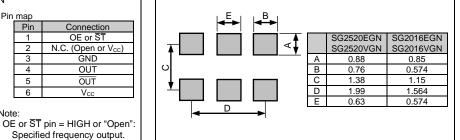
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OE or \overline{ST} pin = LOW:

Output is high impedance

Note:



In order to achieve optimum jitter performance, it is recommended that 0.1 μF and 10 μF bypass capacitors should be connected between V_{CC} and GND and placed as close to the V_{CC} pin as possible.

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