TCXO HIGH STABILITY 105 °C HIGH TEMPERATURE





Product Number

TG7050CKN: X1G005661xxxx99 TG7050SKN: X1G005671xxxx99 TG7050CMN: X1G005681xxxx99 TG7050SMN: X1G005691xxxx99

TG7050CKN / TG7050SKN TG7050CMN / TG7050SMN

Frequency range : 10 MHz to 54 MHz
 Supply voltage : 3.3 V Typ.
 Frequency / temperature characteristics

: $\pm 0.1 \times 10^{-6}$ Max. (-40 °C to +105 °C)

Free-run accuracy
 £4.6 × 10-6 Max. / 20 years (for Stratum3)
 External dimensions
 7.0 × 5.0 × 1.5 mm (10 pins or 4 pins)
 Network synchronization, Stratum3, BTS, SyncE, IEEE1588, Microwave, BTS
 Features
 105 °C High temp, High stability







Specifications (characteristics)

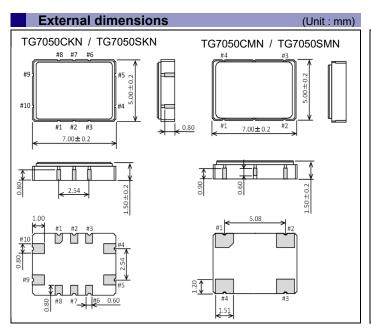
Item	Symbol	CMOS	Clipped sine wave	Condition
Output frequency range	fo	10 MHz to 54 MHz		Please contact us about available frequencies.
Supply voltage	V _{CC}	3.3 V	± 5 %	
Storage temperature	T stg	-40 °C to +105 °C		Storage as single product.
Operating temperature	T_use	-40 °C to +105 °C		
a) Frequency tolerance	f tol	±1.0 × 10 ⁻⁶ Max.		After reflow, +25 °C
b) Frequency/temperature characteristics	fo-Tc	±0.1 × 10 ⁻⁶ Max.		-40 °C to +105 °C
c) Frequency/load coefficient	fo-Load	±0.1 × 1	0 ⁻⁶ Max.	Load ± 10 %
d) Frequency/voltage coefficient	fo-V _{CC}	±0.1 × 1	0-6 Max.	V _{CC} ± 5 %
a) Fraguency aging	fore	±0.5 × 10 ⁻⁶ Max.		Please contact us about available frequencies. Storage as single product. After reflow, +25 °C -40 °C to +105 °C Load ± 10 %
e) Frequency aging	f_age	±3.0 × 10 ⁻⁶ Max.		+25 °C, 20 years
Holdover stability		±0.01 × 10⁻ Max.	(+25 °C, 24 hours)	After 10 days of continuous operation
(Constant temperature)	-	±0.04 × 10 ⁻⁶ Max. (+25 °C, 24 hours)		After 48 hours of continuous operation
Wander generation (MTIE, TD	DEV)	Compliant with GR-	1244CORE, ITU-T G.8262	
Free-run accuracy	-	±4.6 × 10 ⁻⁶ M	ax. / 20 years	
		7.0 mA Max.		10 MHz ≤ fo ≤ 26 MHz
Current consumption	I _{cc}	9.0 mA Max.	6.0 mA Max.	26 MHz < fo ≤ 40 MHz
		10.0 mA Max.		40 MHz < fo ≤ 54 MHz
Symmetry	SYM	45 % to 55 %	-	GND level (DC cut)
Output voltage	V _{OH}	90 % V _{CC} Min.	-	
Output voltage	V_{OL}	10 % V _{CC} Max.	-	This includes Item a), b), c), d) and e) 10 MHz ≤ fo ≤ 26 MHz 26 MHz < fo ≤ 40 MHz 40 MHz < fo ≤ 54 MHz GND level (DC cut) 10 % Vcc to 90 % Vcc level, Load: 15 pF
Rise time / Fall time	tr/tf	8.0 ns Max.	-	
Start-up time	t_str	5 ms.		
Output level	Vpp	-	0.8 V Min.	Peak to Peak
Output load condition	Load	15 pF	10 kΩ // 10 pF	
anut voltago	V _{IH}	70% V _{CC} Min.		
Input voltage	V_{IL}	30% V _{CC} Max.		OE terminal (Enable voltage)

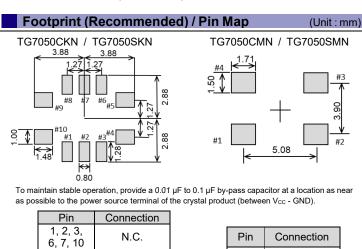
^{*} Note : Please contact us for requirements not listed in this specification.

①Model ②Output (C: CMOS, S: Clipped sine wave) ③Package type (K: 10 pins, M: 4 pins) ④Frequency ⑤Supply voltage (C: 3.3 V Typ.)

©Frequency / temperature characteristics (A: ±0.1 × 10⁻⁶ Max.) ⑦Operating temperature (H: -40 °C to +105 °C)

®OE function (H: Active High, N: Non) 9Vc function (G: Vc Non) ®Internal identification code ("A" is default)





N.C

GND

OUT

 V_{CC}

2

3

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OE p	n = "H" or "open	": Specified frequency	output.
OF n	in = "I " · Output i	is high impedance	

4

8

GND

OUT

OE

PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

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►Pb free.



► Complies with EU RoHS directive.

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Contains Pb in products exempted by EU RoHS directive.





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